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APPLICANTS Masanori Tanimura, Kyoto, JAPAN; Torayuki Tsukada, Kyoto, JAPAN; Kousaku Tanaka, Kyoto, JAPAN;				
** CONTINUING DATA ***** This application is a 371 of PCT/JP2005/005190 03/23/2005				
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35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	JAPAN	11
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ADDRESS HAMRE, SCHUMANN, MUELLER & LARSON, P.C. P.O. BOX 2902 MINNEAPOLIS, MN 55402-0902 UNITED STATES				
TITLE Chip Resistor and Manufacturing Method Thereof				
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